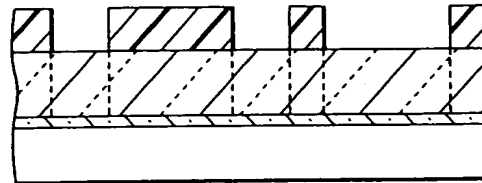
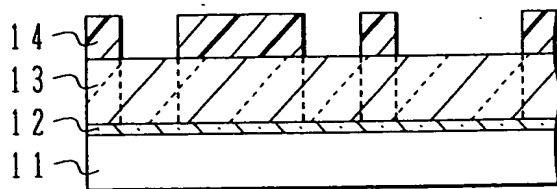


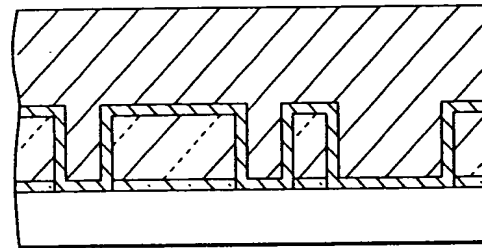
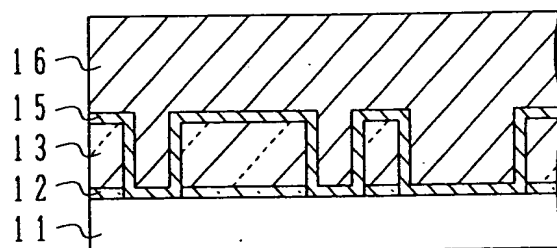
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

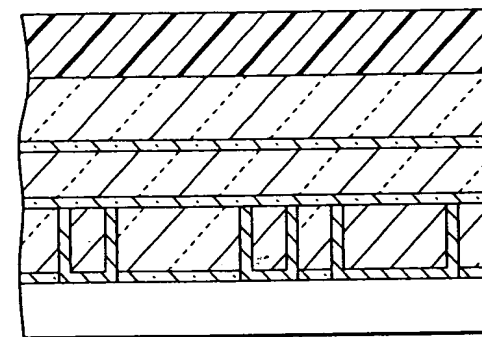
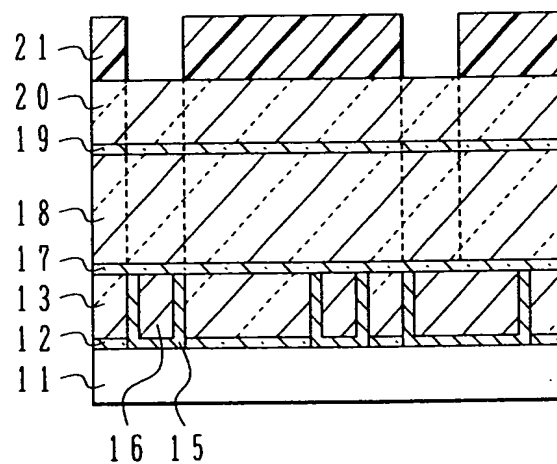
**FIG. 1A**



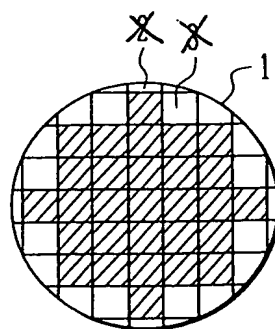
**FIG. 1B**

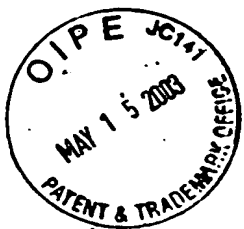


**FIG. 1C**



**FIG. 1D**



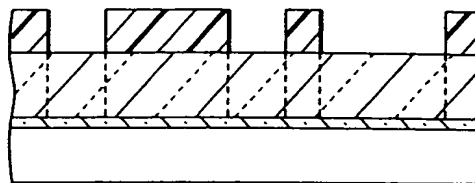
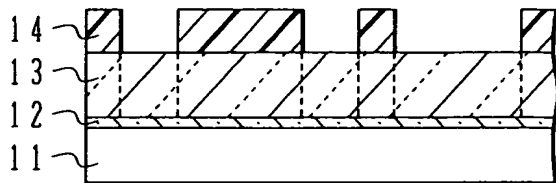


-- RELATED ART --

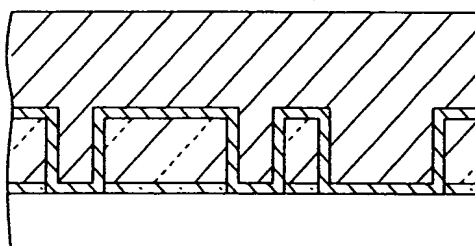
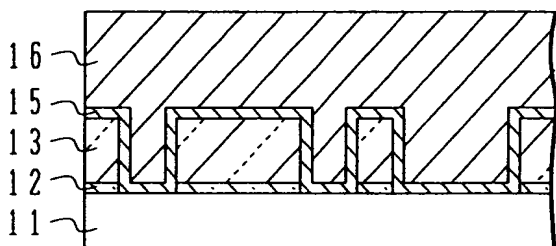
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

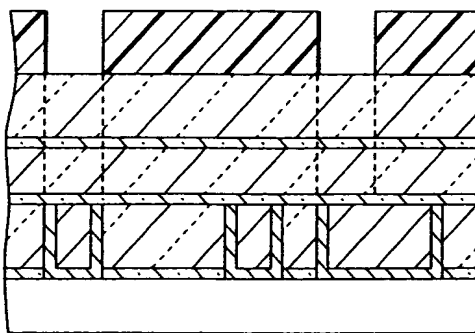
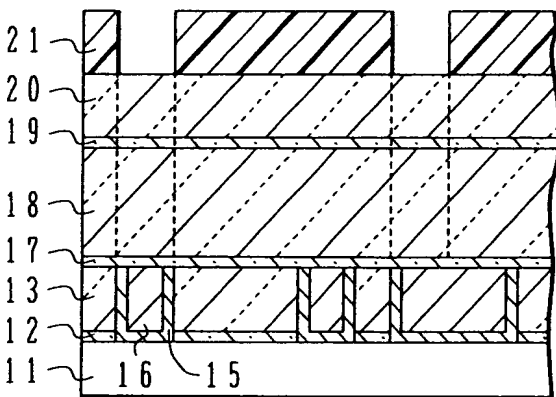
**FIG.12A**



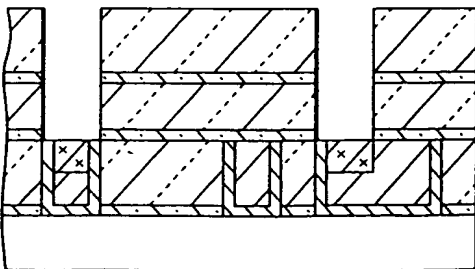
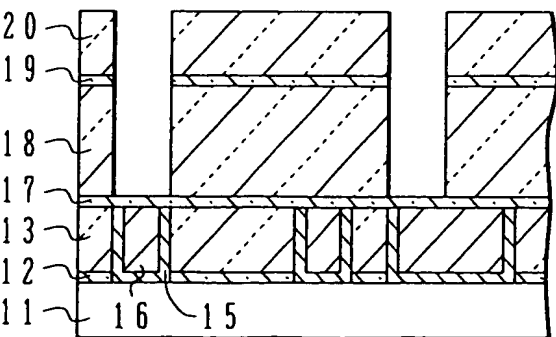
**FIG.12B**



**FIG.12C**

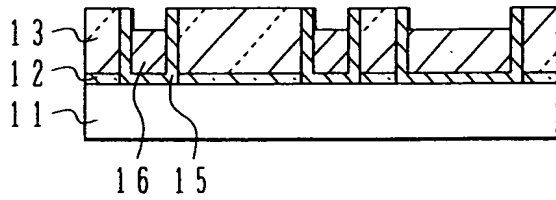


**FIG.12D**

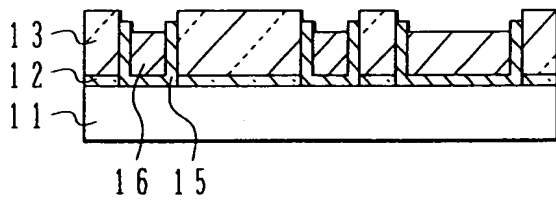


-- RELATED ART --

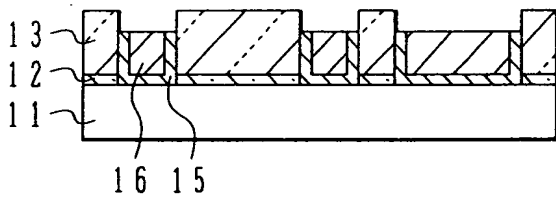
**FIG.13A**



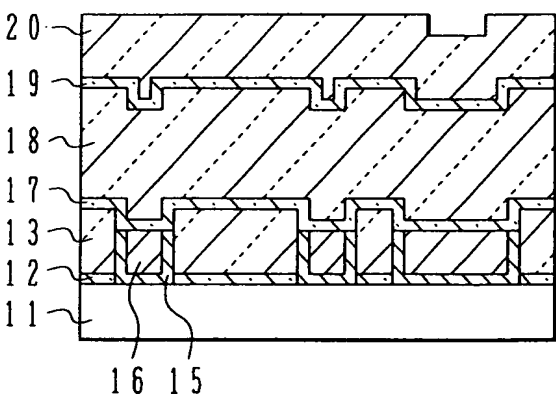
**FIG.13B**



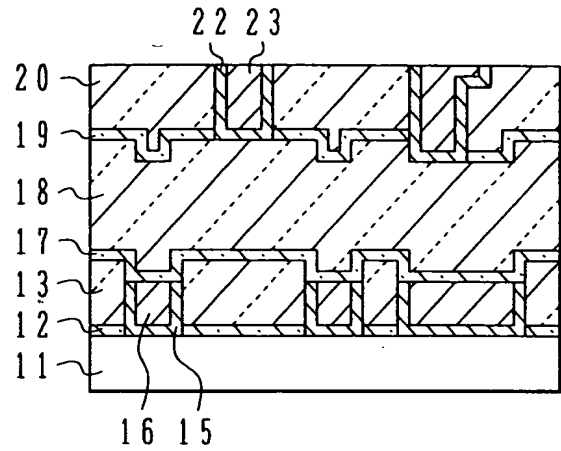
**FIG.13C**



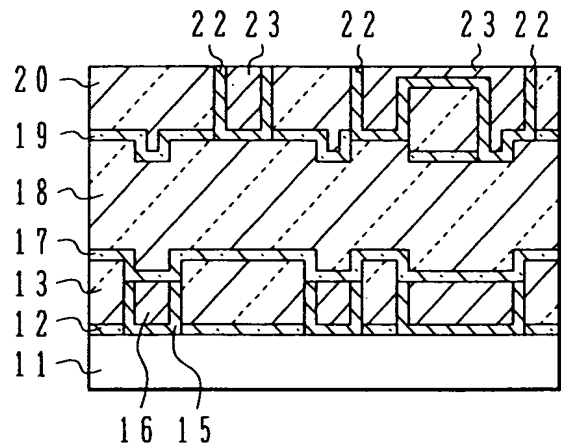
**FIG.13D**



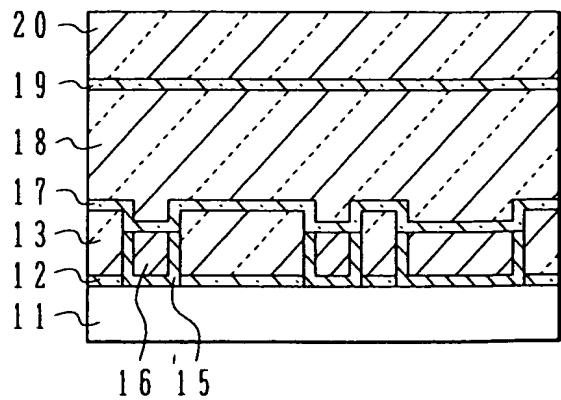
**FIG.13E**



**FIG.13F**



**FIG.13G**



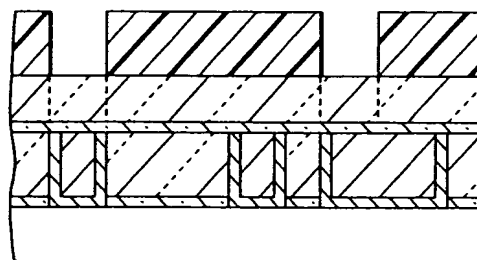
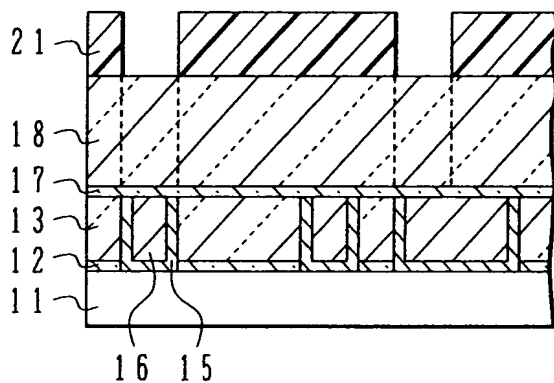


-- RELATED ART --

EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

**FIG.14A**



**FIG.14B**

